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[Chip Scale Jul-Aug '00 5/31/00 08:33 Page 81 Comparing ...](#)

Figure 3 Shows A High I/O-flip-chip Device At 178-Åµm Pitch Full Area Array. Future Production Area Array Pitches Will Continue To Reduce To 150-Åµm In The Next

[SCCR – Overall Panel Short Circuit Current Rating](#)

- 1 - 1 IEC = International Electrical Commission 2 NRTL = Nationally Recognized Testing Laboratory 3 CEC = Canadian Electrical Code SCCR - Overall Panel Short Circuit Current Rating

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Atomic Layer Deposition: An Overview Steven M. George* Department Of Chemistry And Biochemistry And Department Of Chemical And Biological Engineering, University Of Colorado,

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